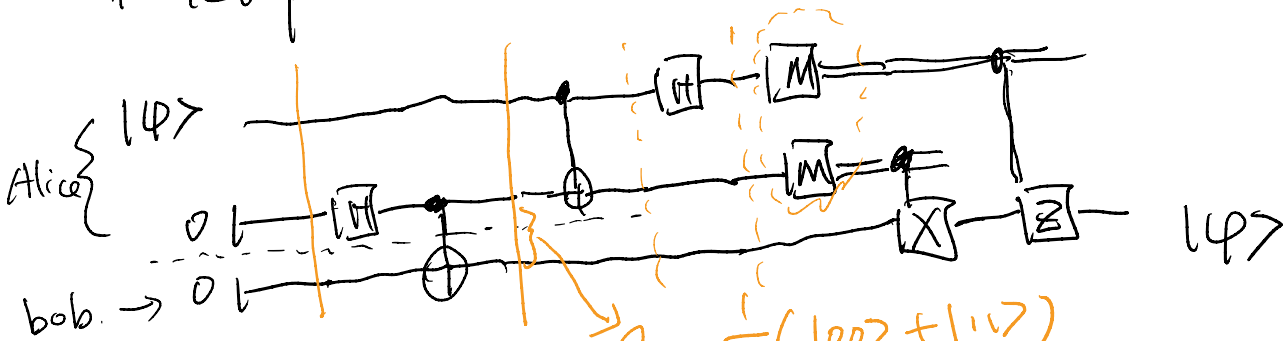




1/23/2025

\* Teleportation Circuit.



Here is why:

$$|\beta_{00}\rangle = \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle)$$

$$|\psi\rangle = a|0\rangle + b|1\rangle, \quad a, b \in \mathbb{C}, \quad |a|^2 + |b|^2 = 1$$

$$|\psi\rangle |00\rangle \mapsto |\psi\rangle \otimes \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle)$$

$$= (a|0\rangle + b|1\rangle) \otimes \frac{1}{\sqrt{2}}(|00\rangle + |11\rangle)$$

$$= \frac{1}{\sqrt{2}} (a|000\rangle + a|011\rangle + b|100\rangle + b|111\rangle)$$

$$CNOT_{1 \rightarrow 2} \otimes Id$$

$$\mapsto \frac{1}{\sqrt{2}} (a|000\rangle + a|011\rangle + b|110\rangle + b|101\rangle)$$

$$H \otimes Id \otimes Id$$

$$\mapsto \frac{1}{\sqrt{2}} (a|+\rangle \otimes |00\rangle + a|+\rangle \otimes |11\rangle$$

$$+ b|-\rangle \otimes |10\rangle + b|-\rangle \otimes |01\rangle)$$

$$= \frac{1}{\sqrt{2}} \left( a \frac{1}{\sqrt{2}} (|0\rangle + |1\rangle) \otimes |00\rangle + a \frac{1}{\sqrt{2}} (|0\rangle + |1\rangle) \otimes |11\rangle \right.$$

$$\left. + b \frac{1}{\sqrt{2}} (|0\rangle - |1\rangle) \otimes |10\rangle + b \frac{1}{\sqrt{2}} (|0\rangle - |1\rangle) \otimes |01\rangle \right)$$

$$= \frac{1}{2} (a|000\rangle + a|100\rangle + a|011\rangle + a|111\rangle + b|010\rangle - b|110\rangle + b|001\rangle - b|101\rangle)$$

$$= \frac{1}{2} (|00\rangle \otimes (a|0\rangle + b|1\rangle)$$

$$+ |01\rangle \otimes (a|1\rangle + b|0\rangle)$$

$$+ |10\rangle \otimes (a|0\rangle - b|1\rangle)$$

$$+ |11\rangle \otimes (a|1\rangle - b|0\rangle)$$

$M \otimes M \otimes Id$

①  $|00\rangle$

$|\varphi\rangle$  is the state of the 3rd qubit.

②  $|01\rangle$

Bob has to apply a X gate to recover  $|\varphi\rangle$ .

③  $|10\rangle$

Bob has to apply a Z gate to recover  $|\varphi\rangle$ .

④  $|11\rangle$ , Bob has to first apply X, then apply Z to get  $|\varphi\rangle$ .

\* Well-known facts and definitions.

① A gate set  $S$  is universal for quantum computing for any unitary operation  $U$ , and for any precision  $\epsilon$ , there exists a circuit  $C$  constructed from  $S$  s.t.  $\|C - U\| < \epsilon$ .

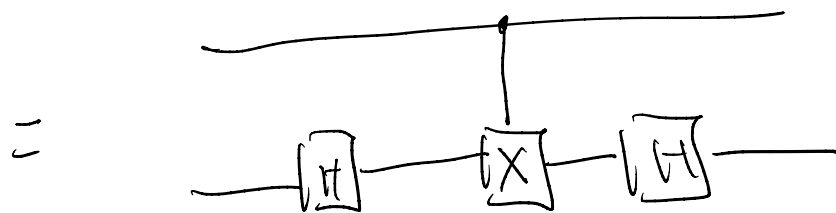
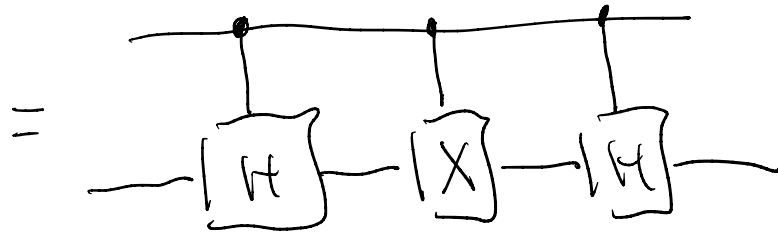
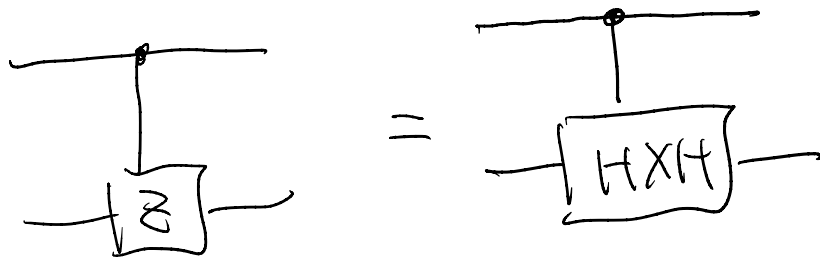
\* We define the Clifford circuits, to be the set of circuits constructed from the gate set  $\{H, S, CNot\}$ .

$Z$  gate is Clifford because  
 $S^2 = Z$ .

$X$  gate is also Clifford because

$$X = H \circ Z \circ H \quad (\text{leave as an exercise.})$$
$$\Downarrow$$
$$H X H = Z$$

CZ gate is also Clifford.



So CZ is Clifford.

Other important properties :

① Clifford circuits can be efficiently simulated by a classical computer.

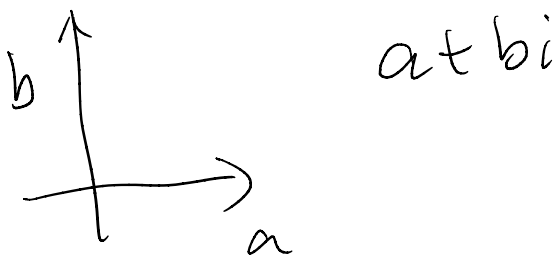
Note: T gate and Toffoli gate are not Clifford.

\* Some examples of universal gate set.

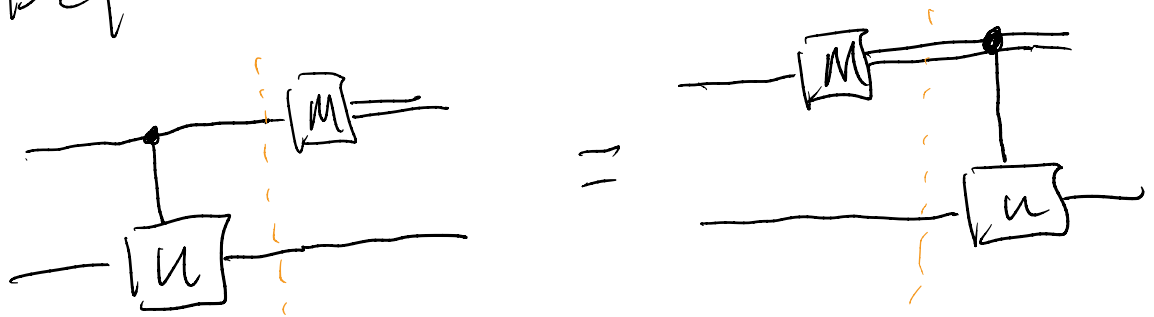
① Clifford + T.

② Clifford + CS (controlled S gate).

③  $\{ \text{Toffoli}, H \}$



\* Defer measurement principle.



$$|\varphi\rangle = a_1|00\rangle + a_2|01\rangle + a_3|10\rangle + a_4|11\rangle$$

LHS:  $|\varphi\rangle \xrightarrow{\text{controlled } U}$

$$a_1|00\rangle + a_2|01\rangle + a_3|1\rangle \otimes U|0\rangle + a_4|1\rangle \otimes U|1\rangle$$

$$= |0\rangle \otimes (a_1|0\rangle + a_2|1\rangle) + |1\rangle \otimes (a_3U|0\rangle + a_4U|1\rangle)$$

$$\begin{array}{l} \xrightarrow{M \otimes Id} \\ \left\{ \begin{array}{l} \textcircled{1} |0\rangle, \text{ then } a_1|0\rangle + a_2|1\rangle \\ \textcircled{2} |1\rangle, \text{ then } a_3U|0\rangle + a_4U|1\rangle \end{array} \right. \end{array}$$

$$|\psi\rangle = a_1|00\rangle + a_2|01\rangle + a_3|10\rangle + a_4|11\rangle.$$

$$\text{RHS: } |\psi\rangle \xrightarrow{M \otimes Id} \left\{ \begin{array}{l} \text{if } 0, \underline{a_1|0\rangle + a_2|1\rangle} \\ \text{if } 1, \begin{array}{l} a_3|0\rangle + a_4|1\rangle \\ \text{apply } U \end{array} \end{array} \right.$$

$$= a_3U|0\rangle + a_4U|1\rangle$$