

CSCE 212 Spring 2018 Schedule

Class	Date	Day	Topic	Reading	Lab Assignments
1	16-Jan	Tue	Overview	1.1	
2	18-Jan	Thu	Data Representation - Integers	2.1-2.2	Linux Boot Camp
3	23-Jan	Tue	Integers II	2.2-2.3	
4	25-Jan	Thu	IEEE 754 Floating point representation	2.4-2.5	Datalab
5	30-Jan	Tue	Instruction Set Architecture	3.1-3.5	
6	1-Feb	Thu	Control structures	3.6	BigExpFloats
7	6-Feb	Tue	Procedures	3.7	
8	8-Feb	Thu	Arrays, pointers, etc.	3.8-3.9	Bomb Lab
9	13-Feb	Tue	Dynamic Memory, buffer overflow	3.1	
10	15-Feb	Thu	Memory Hierarchy	6.1-6.3	Attack Lab
11	20-Feb	Tue	Cache memory	6.4	
12	22-Feb	Thu	Test 1		
13	27-Feb	Tue	Y86-64 Instruction Set Architecture (ISA)	4.1	
14	1-Mar	Thu	Hardware Description Languages (HDL)	4.2	Cache Lab
15	6-Mar	Tue	Sequential Y86-64 Implementation	4.3	
16	8-Mar	Thu	Sequential Y86-64 Implementation II		Arch Lab Part A
	9-Mar	Fri	Last Day to withdraw with "W"		
	13-Mar	Tue	Mar 11-18, Sun-Sun Spring break		
	15-Mar	Thu	Mar 11-18, Sun-Sun Spring break		
17	20-Mar	Tue	Pipelining	4.4	
18	22-Mar	Thu	Pipelined Y86-64 Implementation	4.5	Arch Lab Part B
19	27-Mar	Tue	Pipelined Y86-64 Implementation II	4.5	
20	29-Mar	Thu	Pipelined Y86-64 Implementation III	4.5	Arch Lab Part C
21	3-Apr	Tue	Optimization	5.1-5.7	
22	5-Apr	Thu	Test 2		
23	10-Apr	Tue	Optimization	5.1-5.7	
24	12-Apr	Thu	Linking. Libraries, Shared libraries	7.1-	
25	17-Apr	Tue	Virtual Memory	9.1-9.5	
26	19-Apr	Thu	Address Translation	9.6-9.8	Malloc Lab
27	24-Apr	Tue	Dynamic memory, Garbage collection	9.9-9.10	
28	26-Apr	Thu	Ken Thompson Turing Award Lecture		
	30-Apr	Mon	Last Day of classes		
	1-May	Tue	Reading Day		
	8-May	Tue	Exam - Tuesday, May 8 – 9:00 a.m		