

3. The following fragment of 3-address code was produced by a non-optimizing compiler:

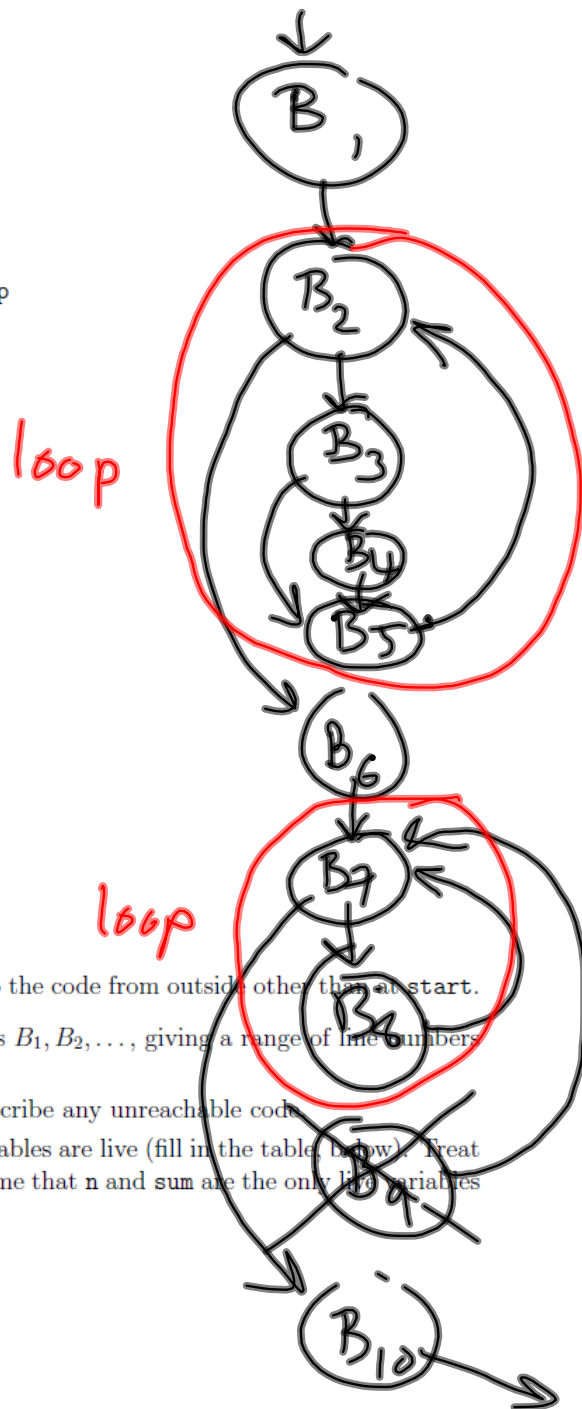
B_1	1	start:	$x := 1$
	2		$y := 1$
	3		$sum := x$
	4		$sum := sum + y$
B_2	5	loop:	if $x = n$ then goto out2
	6		$t1 := y$
B_3	7		$t2 := t1 * t1$
	8		$t3 := t1 + x$
B_4	9		if $t3 < n$ then goto skip
	10		$t3 := t3 - n$
B_5	11	skip:	$sum := sum + t3$
	12		$y := y + 1$
B_6	13		goto loop
	14	out2:	$x := sum$
B_7	15		$sum := x + 1$
	16		$x := x + 1$
B_8	17	out1:	$x := x - 1$
	18		$t1 := x$
	19		print $t1$
	20		if $x = 0$ then goto out

B_9	21		goto out1
	22		goto out1
B_{10}	23	out:	no_op

Assume that there are no entry points into the code from outside other than at start.

- Decompose the code into basic blocks $B_1, B_2, \dots$, giving a range of line numbers for each.
- Draw the control flow graph, and describe any unreachable code.
- For each control point, list which variables are live (fill in the table below). Treat the array a as a single variable. Assume that n and sum are the only live variables immediately before line 24.

Before line	Live variables
1	
2	
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	



code generator → intermediate code → code optimizer → intermediate code

optimizer → code generator

Before intermediate code
Shared subexpressions.

$$a = i * b + c$$

to fresh temporary

$$t_1 = i * b$$

$$t_2 = t_1 + c$$

$$a = t_2$$

$$a + (b - a) + (b - a) * d$$

Syntax DAG

algebraic transformations

$$t_1 = b - a$$

$$t_2 = a * t_1$$

$$t_3 = t_1 + d$$

$$t_4 = t_2 + t_3$$

$$a = b * \text{DEBUG}$$

$$a = 0$$

Hydra DEBUG 0

Intermediate code optimizations

- peephole
- global

$$a = i * b + c$$

$$t_1 = i * b$$

$$a = t_1 + c$$

$$a = t_1$$

goto L₂

...

L₁: goto L₂

...

if x goto L₂

...

L₁ goto L₂

if 0 != 1 goto L₂ goto L

L₁:

→ {

L:

goto L

L:

global analysis:

- control flow analysis
- basic blocks & flow graph
- liveness analysis

Basic block: Given 3-address code

A basic block is a sequence of contiguous instructions whose control can only enter at the beginning and exit at the end. (a normal block)

Control flow graph:

Digraph

vertices are the basic blocks

directed edge

if control can flow from B_i directly into B_j

Uses:

- Detect unreachable code
- Merge blocks: if no other edges out of B or into C

Detect loops

A loop is a set of vertices in a control flow digraph satisfying

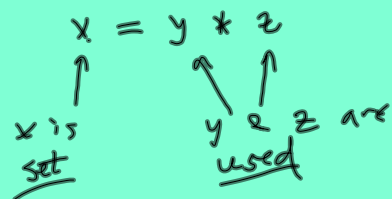
1. L is strongly connected
{can follow a path in L from any node in L to any other node in L }
2. L has a unique entry point

An inner loop is a loop that has no proper subloops.

most execution time is spent in inner loops.

concentrate optimization here.

Liveness analysis



at any given point in a program: a variable is live (or alive) if there is some control flow path to a spot where that variable is used with no intervening set of the variable. Otherwise, var is dead.

